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PART II – SPE Models

Software Execution Models



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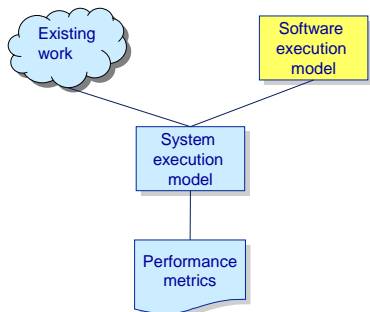
Outline

- Purpose and properties of software execution models (SEM)
- Execution graph representation
- Solving software execution models
- Case study



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SPE Models





Purpose of Software Execution Models

- Early modeling is essential to ensure that the software architecture will meet performance objectives
- Problem - Early in the development process we do not have sufficient knowledge to model performance precisely
- **Solution - Construct the simplest possible models that capture essential performance characteristics**



Properties of Software Execution Models

- Software execution model characterizes resource requirements of the proposed software alone, in the absence of
 - other workloads
 - multiple users
 - delays due to contention for resources
- It provides analysis of
 - **best-case response times**
 - **worst-case response times**
 - **average response times**



Properties of Software Execution Models

- Software execution models can identify serious performance problems at early design phases
 - If the predicted performance is unsatisfactory there is no need to build system execution model
- The absence of problems in software execution model does not mean that there are no problems
 - Contention for system resources could cause problems
 - These problems may be corrected with
 - Software design alternatives
 - Hardware configuration alternatives



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Execution graph representation

- SEM are represented with execution graphs
 - For each key performance scenario we construct execution graph
- Execution graphs are based on elementary graph theory
 - Nodes represent processing steps (program statements that perform a function)
 - Arcs represent the order of execution
- Execution graphs are similar to program flowcharts but not the same; they show
 - only those paths that are key to performance
 - frequency of path execution



Execution graph representation - contd

- It is possible to construct different execution graphs that represent the same software
- These graphs may differ in representation of
 - Software hierarchy
 - Details of abstraction
- There is no single "right way"

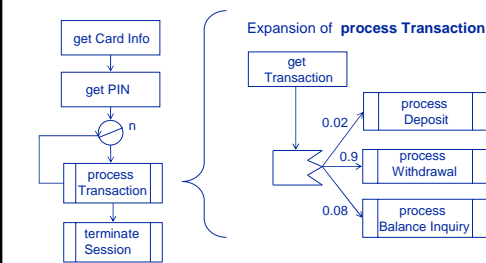


General ATM sequence diagram - contd

- Reference is most easily represented as an expanded node
- For scenarios that involve multiple threads of control or distributed objects more effort is needed to account for communication and synchronization delays



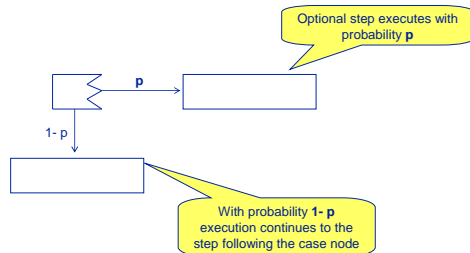
Execution graph for general ATM sequence diagram

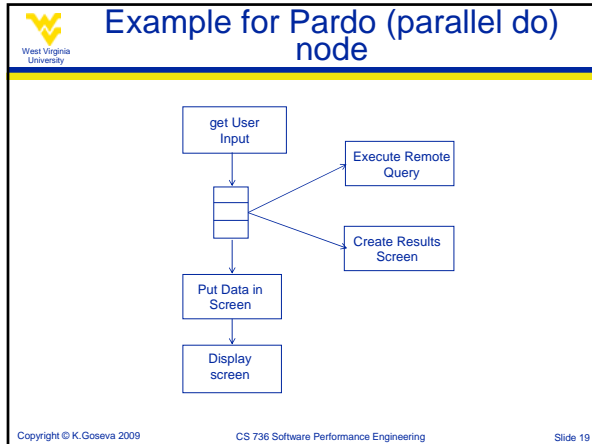


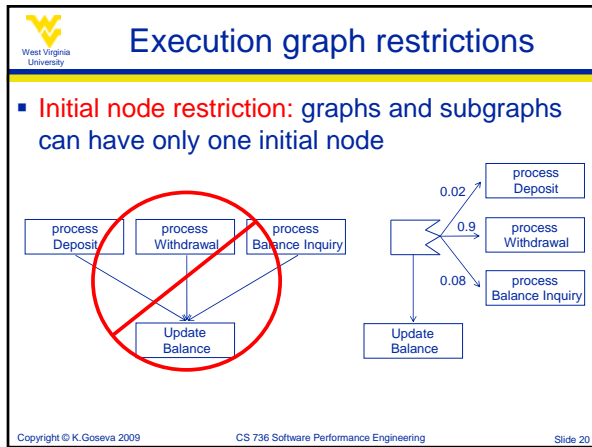


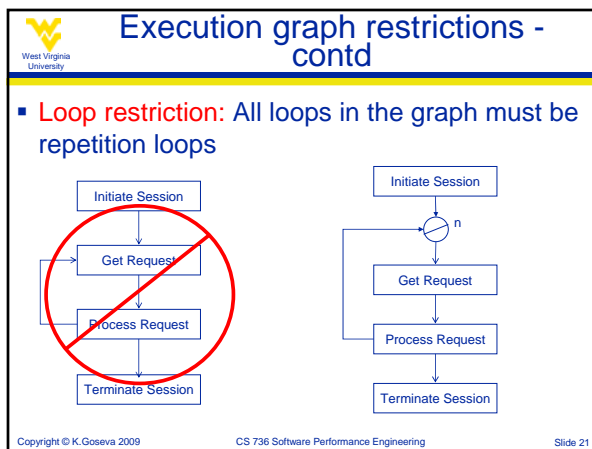
Case node for optional step

- Probabilities of conditional paths on the case node need not sum to 1











Execution graph restrictions - contd

- Restrictions do not restrict the modeling power of the execution graphs – for each graph that violates restrictions there is an equivalent legal representation
- Restrictions simplify the solution algorithms and enable quick solution of many alternatives



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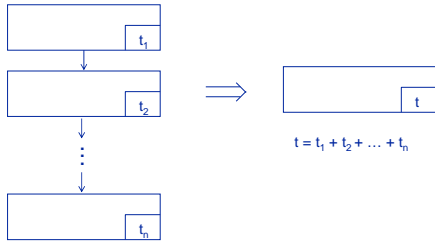
Solving SEM – reduction rules

- Graph reduction method
 - Identify basic structure
 - Compute the time for the structure
 - Replace the structure with a single node whose “time” is the computed time
- Basic structures
 - Sequences
 - Loops
 - Cases



Sequential structure

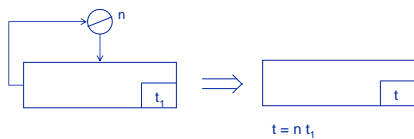
- Sequential structure – sum of the times of the nodes in sequence





Loop structure

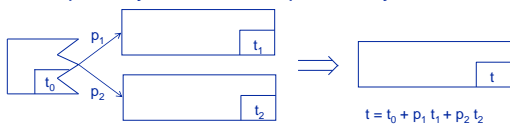
- Loop structure – multiply node time with the loop repetition factor





Case structure

- Computation differs for the shortest path, longest path, and average analysis
 - Shortest path – minimum of the times for the conditionally executed nodes
 - Longest path – maximum of the times for the conditionally executed nodes
 - Average analysis – sum of each node's time multiplied by its execution probability





Parallel structure

- Best case – use the longest of the concurrent paths (other parallel paths are complete when the longest concurrent path completes)
- Worst case – sum of the concurrent paths (serialize the parallel paths, i.e., when one path completes the next begins)



Illustration for reduction rules - ATM example

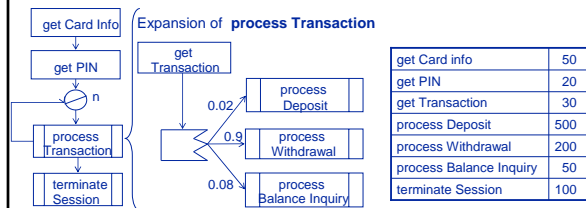




Illustration for reduction rules - ATM example

Process Transaction:

Shortest path = get Transaction + ALT + process Balance Inquiry = 30 + 50 = 80
 Longest path = get Transaction + ALT + process Deposit = 30 + 500 = 530
 Average path = 30 + 0.02*500 + 0.9*200 + 0.08*50 = 224

Session:

Shortest path = get Card Info + get PIN + n*process Transaction-SP + terminate Session
 = 50 + 20 + 2*80 + 100 = 330
 Longest path = get Card Info + get PIN + n*process Transaction-LP + terminate Session
 = 50 + 20 + 2*530 + 100 = 1,230
 Average path = get Card Info + get PIN + n*process Transaction-AV + terminate Session
 = 50 + 20 + 2*224 + 100 = 618



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Case study - ICAD

- Interactive computer-aided design (ICAD) application used to
 - draw the model structures such as aircraft wings
 - store a model in a database (several versions of the model may exist within the database)
 - interactively assess the design's correctness, feasibility, and suitability
- ICAD drawing consists of
 - Nodes – position (x,y,z) and additional information
 - Elements
 - Beams – connect two nodes
 - Triangles – connect three nodes
 - Plates – connect four or more nodes

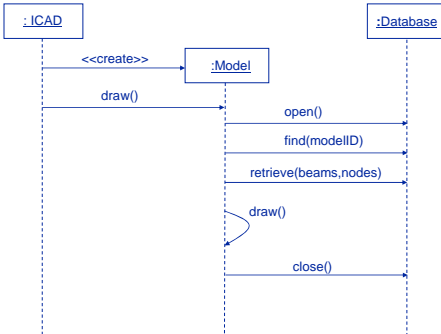


ICAD use cases

- ICAD use cases
 - **Draw** – draw a model
 - **Solve** – solve a model
- We will focus on **Draw** use case and its scenario **DrawMod**
 - Typical model consists of nodes and 2,000 beams (no triangles or plates)
 - Performance objective is to draw a typical model in 10 seconds or less



High-level sequence diagram of DrawMod scenario



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Architecture 1

- Use an object to represent each node and element
 - Flexible
 - All types of elements can be treated in a uniform way
 - New types of elements can be added without changing any other aspect of the application

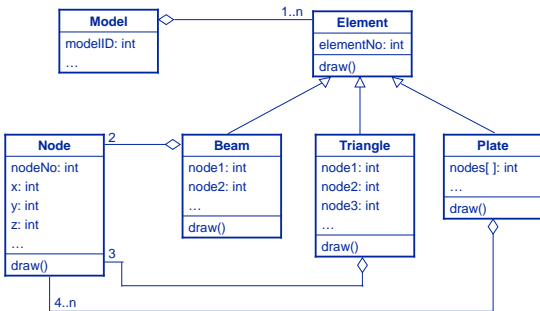
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Architecture 1 – class diagram



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Specify software resource requirements

- Software resources for ICAD example
 - **EDMS** – number of calls to the DB process
 - **CPU** – estimate of the number of instructions executed
 - **I/O** – number of disk accesses to obtain data from DB
 - **Get/Free** – number of calls to the memory management operations
 - **Screen** – number of times graphics operations “draw” to the screen
- We need
 - values for software resource requirements for each processing step in the software execution model
 - number of loop repetitions
 - probability for each case alternative (does not exist in this example)



Values for DrawMod software resource requirements

Processing steps	EDMS	CPU	I/O	Get/Free	Screen
createModel	0	2	0	0	0
drawModel	0	1	3	2	2
openDB	1	2.3	6	1	0
findBeams	1	346	7.08	0	0
sortBeams	1	339	42.28	2	0
close	1	1.5	2	1	0
retrieveBeam	1	2	4.03	0	0
createBeam	0	2	0	0	0
findNodes	1	4.5	4.1	0	0
setupNode	1	4	4.02	0	0
drawNode	0	0.55	0	0	1

* One table for software resource requirements is given instead of separate tables associated with each step



Values for DrawMod computer resource requirements

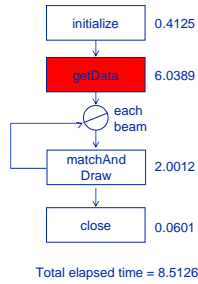
Devices	CPU	Disk	Display
Quantity	1	2	1
Service Units	K Instr.	Phys. I/O	Units

EDMS	0.253	0.002	
CPU	1		
I/O	0.1	1	
Get/Free	0.1		
Screen	0.05		1

Service time	0.000005	0.03	0.001
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Execution graph and results for Architecture 2





Modeling Hints

- Combine related steps that do not have significant effect on performance
 - SEM is an abstraction which includes only details that are relevant to performance
- Use hierarchy
 - Models are easier to understand and modify
 - Expand nodes as your knowledge of the software increases



Modeling Hints - contd

- Use best-case and worst-case estimates for resource requirements
 - If the best-case results indicate that there is a problem, fix it before proceeding
 - If the worst-case results indicate that there is no problem, proceed
 - If there is a problem, look at the processing steps that consume the most resources



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Modeling Hints - contd

- Study the sensitivity of the performance results to the input parameters; identify critical resources and components whose use of these resources should be monitored
- Sensitivity may due to
 - Processing in loops
 - Significant synchronization and resource-sharing delays
